



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Information Technology**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech (CSE/IT)**

**SEMESTER - I ( 2025-2029)**

| Sr. No. | Course Code | Course Name  | TEACHING |   |    | CREDITS | EXAMINATION SCHEME            |                     |                            |                               |                            | TOTAL MARKS |
|---------|-------------|--|----------|---|----|---------|-------------------------------|---------------------|----------------------------|-------------------------------|----------------------------|-------------|
|         |             |  | L        | T | P  |         | THEORY                        |                     |                            | PRACTICAL                     |                            |             |
|         |             |  |          |   |    |         | End Sem University Exam (60%) | Two Term Exam (20%) | Teachers Assessment* (20%) | End Sem University Exam (60%) | Teachers Assessment* (40%) |             |
| 1       | BTCSH102    | Statistics, Probability and Calculus                 | 3        | 0 | 0  | 3       | 60                            | 20                  | 20                         | 0                             | 0                          | 100         |
| 2       | BTEC104     | Digital Logic & Circuit Design                       | 3        | 1 | 2  | 5       | 60                            | 20                  | 20                         | 30                            | 20                         | 150         |
| 3       | BTCS101M    | Introduction to Computer Science and Engineering     | 3        | 0 | 0  | 3       | 60                            | 20                  | 20                         | 0                             | 0                          | 100         |
| 4       | BTCS102M    | Introduction to Design Thinking                      | 2        | 0 | 2  | 3       | 60                            | 20                  | 20                         | 30                            | 20                         | 150         |
| 5       | BTCS103M    | Computer System Organization                         | 3        | 0 | 2  | 4       | 60                            | 20                  | 20                         | 30                            | 20                         | 150         |
| 6       | BTCS107M    | Program development using C                          | 0        | 0 | 2  | 1       | 0                             | 0                   | 0                          | 30                            | 20                         | 50          |
| 7       | BTCS101M(P) | Introduction to Computer Science and Engineering Lab | 0        | 0 | 2  | 1       | 0                             | 0                   | 0                          | 0                             | 50                         | 50          |
|         |             |  | 14       | 1 | 10 | 20      | 300                           | 100                 | 100                        | 120                           | 80                         | 700         |

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

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Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore